**For Implementing Interrupts:**

Added INT val flag to latch values for indicating an interrupt.

Generated timer INT at cycle 300 in sim that sets INT flag and populates INTV field.

Adding micro sequencer INT handling. (Adding PSR to system latches)

The simulator will assert an interrupt signal which will choose the first state of the interrupt handling states. So basically like a mux right before microsequencer output that says if int go to int states, else do normal calculated state.

Need to add interrupt generation in simulator, at 300 cycles assert int flag ans set int register.